

IN THE CLAIMS:

Claim 1. **(Original)** A register setting method for setting operation condition information, which defines the operation of a device, in a first register and a second register, the method comprising the steps of:

storing first operation condition information in the first register;

storing second operation condition information in the second register;

changing the first operation condition information; and

when the first operation condition information is changed, changing the second operation condition information in accordance with change information for changing the first operation condition information.

Claim 2. **(Original)** The register setting method according to claim 1, wherein:
the device generates a start signal for starting the device; and the method further comprising the step of:

loading the second operation condition information stored in the second register into the first register in response to the start signal.

Claim 3. **(Original)** The register setting method according to claim 1, wherein:
the first register generates a device control signal for controlling the operation of the device, the device control signal including the change information; and
the step of changing the second operation condition information includes the steps of:

detecting a change in the device control signal; and

when a change in the device control signal is detected, storing second operation condition information in the second register in response to the device control signal, the second operation condition information being identical to changed first operation condition information.

Claim 4. **(Original)** The register setting method according to claim 3, wherein:

the device generates a start signal for starting the device; and

the method further comprises the step of:

loading the first register with the second operation condition information stored in the second register in response to the start signal.

Claim 5. **(Original)** The register setting method according to claim 1, wherein:

the first register receives a set signal including the change information; and

the step of changing the second operation condition information includes the steps of:

detecting a change in the first operation condition information stored in the first register; and

when a change in the first operation condition information is detected, storing information identical to changed first operation condition information in response to the set signal.

Claim 6. **(Original)** The register setting method according to claim 5, wherein:
the device generates a start signal for starting the device; and
the method further comprises the step of:
loading the first register with the second operation condition information stored in
the second register in response to the start signal.

Claim 7. **(Original)** The register setting method according to claim 6, wherein:
the first register receives a mode set signal including the mode change
information, and generates a device control signal for controlling an operation mode of
the device, the device control signal including the mode change information; and
the step of changing the second operation condition information includes the
steps of:
detecting a change in the first operation condition information stored in the first
register; and
when a change in the first operation condition information is detected, storing
information identical to changed first operation condition information in the second
register in response to the device control signal.

Claim 8. **(Original)** The register setting method according to claim 7, wherein:
the device generates a start signal for starting the device; and
the method further comprising:
loading the first register with the second operation condition information stored in
the second register in response to the start signal.

Claim 9. **(Original)** A register setting method for setting operation condition information, which defines the operation of a device, in a first register including a volatile memory and a second register including a non-volatile memory, the method comprising the steps of:

storing first operation condition information in the first register;

storing second operation condition information in the second register;

changing the second operation condition information; and

when the second operation condition information is changed, changing the first operation condition information in the first register substantially at the same time the second operation condition information is changed, in accordance with change information for changing the second operation condition information.

Claims 10-23 **(Canceled)**